



Thematic Network on Silicon on Insulator Technology, Devices and Circuits.
[IST-1-506653-CA]

EUROSIO "Who is Who" Guide

Name of the organisation

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| <i>Organization Legal name:</i> | <i>Centro Nacional de Microelectrónica (Consejo Superior de Investigaciones Científicas)</i> |
| <i>Organization short name:</i> | <i>CNM-CSIC</i> |
| <i>Internet homepage:</i> | <i>www.cnm.es</i> |

Contact person

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|-----------------------|--|----------------|--|
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Experience and expertise fields: CNM has a large experience on Bulk power semiconductor devices, including LDMOS transistors in SOI and SOS substrates. Four fields are covered on SOI: thermal management and new solutions to reduce self-heating effects, high-voltage power LDMOS, very low voltage protection devices (TVS) and advanced power LDMOS structures for RF applications.

Facilities and Equipment: CNM holds a flexible Clean Room with standard CMOS technology (2.5 μm) and several other technologies addressed to sensors, microsystems and power devices. Nanofabrication equipments are also available. Fabricated sensors, ICs and power devices can be packaged and electrically tested at CNM. Simulation tools available are ANSYS, Silvaco, ISE-TCAD, Cadence, Flowtherm and specific software for analogue and digital design.

Three last international research projects:

Advanced Techniques for High Temperature System-on-Chip (ATHIS)

GRD1-2001-40466 – Growth Programme (UE)

Power and Thermal Management of Wide Bandgap Semiconductors AO/ITT A04349 - European Space Agency (ESA –UE)

Establishing Silicon Carbide Applications in Power Electronics in Europe (ESCAPEE)

GRD2-2000-30399 – Growth Programme (UE)