



Thematic Network on Silicon on Insulator Technology, Devices and Circuits.
[IST-1-506653-CA]

EUROSOI “Who is Who” Guide

<i>Name of the organisation</i>	
<i>Organization Legal name:</i>	Chalmers Tekniska Högskola AB
<i>Organization short name:</i>	CHALMERS
<i>Internet homepage:</i>	www.chalmers.se , www.mc2.chalmers.se

<i>Contact person</i>			
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<i>Postal Address</i>	<i>Department of Microtechnology and Nanoscience (MC2) Chalmers University of Technology SE-412 96 Göteborg, Sweden</i>		

Other Senior Researchers: (up to 10 names, please include e-mail address)

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Assist. Prof. Anke Sanz-Velasco, anke.sanz-velasco@mc2.chalmers.se

Experience and expertise fields: (50 words)

Manufacture of SOI and other advanced materials by wafer bonding. Electrical device and materials characterisation. RF device modelling. MEMS structures in SOI. Nanoscaled Si devices on SOI.

Facilities and Equipment:

The facilities of MC2 include 1240 m² state-of-the-art cleanroom offering processing platforms for a variety of materials and technologies. The equipment include 10 nm e-beam lithography as well as processing capabilities for up to 150 mm silicon wafers. In addition to the cleanroom facility a wide range of equipment and tools for electrical characterisation from dc to several hundred Ghz are available in our measurement laboratories. A wide range of physical characterisation equipment is also available at Chalmers primarily outside the Department of Microtechnology and Nanoscience.

Three last international research projects:

- **EU: SINANO**
- **EU: SOICMOS (CMOS SOI for low power logic and RF wireless)**
- **EU: SESIBON**