



Thematic Network on Silicon on Insulator Technology, Devices and Circuits.
[IST-1-506653-CA]

EUROSIO "Who is Who" Guide

Name of the organisation

<i>Organization Legal name:</i>	Interuniversitair Mikro-Electronica Centrum
<i>Organization short name:</i>	IMEC
<i>Internet homepage:</i>	www.imec.be

Contact person

<i>Name:</i>	<i>Claeys</i>	<i>Title:</i>	<i>Prof.</i>
<i>First name:</i>	<i>Cor</i>	<i>Sex:</i>	<i>M</i>
<i>Phone:</i>	<i>+32 16 282328</i>	<i>E-mail:</i>	<i>claeys@imec.be</i>
<i>Postal Address</i>	<i>Kapeldreef 75, B-3001 Leuven, Belgium</i>		

Other Senior Researchers: (up to 10 names, please include e-mail address)

Emmanuel Augendre, augendre@imec.be

Serege Biesemans, biesemans@imec.be

Kristin De Meyer, demeyer@imec.be

Gosia Jurczak, jurczak@imec.be

Eddy Simoen, simoen@imec.be

Experience and expertise fields: (50 words)

IMEC has a long standing experience in design, processing and testing of deep submicron SOI devices. Study of device physics, reliability, LF noise, radiation effects, and low temperature performance. Processing on 200 mm wafers. Present efforts are focussing on FinFETS for the 50 nm technology mode. Also activities on strained Si on SOI and GeOI.

Facilities and Equipment:

Fully equipped 200 mm processing line, and 300 mm in start-up.

Extensive analytical and structural characterization facilities.

Equipment for electrical device characterization, including low frequency noise.

Three last international research projects:

Presently involved in NANOCMOS and SINANO.

NESTOR, ARTEMIS, VAMOS