



Thematic Network on Silicon on Insulator Technology, Devices and Circuits.
[IST-1-506653-CA]

EUROSIO "Who is Who" Guide

Name of the organisation

<i>Organization Legal name:</i>	Institut Supérieur d'Electronique de Paris
<i>Organization short name:</i>	ISEP
<i>Internet homepage:</i>	www.isep.fr

Contact person

<i>Name:</i>	<i>Amara</i>	<i>Title:</i>	<i>Prof.</i>
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Other Senior Researchers:

Prof. Andrei Vladimirescu, Andrei.vladimirescu@isep.fr

Experience and expertise fields: (50 words)

Amara Amara obtained his HDR (Confirmation of Leading Research Capabilities) in 1999 from Evry university, a Ph.D. in computer science in 1989 and a DEA (MSc) in 1984 in microelectronics and computer science both from Paris VI university.

Andrei Vladimirescu received the M.S. and Ph.D. degrees in electrical engineering and computer sciences from the University of California, Berkeley, in 1980 and 1982, respectively.

Fields of Expertise

SOI Integrated Circuits. Modeling and Simulation. Low-voltage, low-power design
Ultra-low voltage operation. Analog, Mixed-signal and RF design and simulation

Facilities and Equipment:

Simulation and Design Lab equipped with

- 20 Sun Workstations (Ultra-10)
- Leading EDA IC design software
- ST 0.13um SOI Design Kits and Standard Cell Library

Three last international research projects:

T206 Medea+ on SOI

TEMPUS

Sanara – Medea+ (Galileo)