



Thematic Network on Silicon on Insulator Technology, Devices and Circuits.
[IST-1-506653-CA]

EUROSIO "Who is Who" Guide

Name of the organisation

<i>Organization Legal name:</i>	STMicroelectronics Central R&D
<i>Organization short name:</i>	ST
<i>Internet homepage:</i>	www.st.com

Contact person

<i>Name:</i>	<i>Tinella</i>	<i>Title:</i>	<i>PhD</i>
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Other Senior Researchers: (up to 10 names, please include e-mail address)

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Experience and expertise fields: (50 words)

RF/analog design and device modeling in SOI partially depleted technologies.

Facilities and Equipment:

Availability of PD SOI process for the 0.13um , 90nm and 65nm technology nodes.

Three last international research projects:

MEDEA+