



Thematic Network on Silicon on Insulator Technology, Devices and Circuits.  
[IST-1-506653-CA]

## EUROSOI "Who is Who" Guide

### *Name of the organisation*

<i>Organization Legal name:</i>	<b>STMicroelectronics</b>
<i>Organization short name:</i>	<b>ST</b>
<i>Internet homepage:</i>	<b>www.st.com</b>

### *Contact person*

<i>Name:</i>	<b>MACKOWIAK</b>	<i>Title:</i>	
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### **Other Senior Researchers:** (up to 10 names, please include e-mail address)

Christine Raynaud [Christine.raynaud@st.com](mailto:Christine.raynaud@st.com)  
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### **Experience and expertise fields:** (50 words)

At ST level:

PD and FD SOI process modules and assembly, active and passive devices DC, AC, RF characterisation and modelling, circuits design for Low Power/Low Voltage SoC (digital, mixed and RF)

Personnal: CMOS (bulk, SOS, SOI) and BICMOS circuits design, test  
process-devices characterisation-modeling  
RF projects management (cmos, bicmos, soi)

### **Facilities and Equipment:**

All facilities and equipments for R&D and production for process generations till 32nm

### **Three last international research projects:**

IST SATURN SOI Advanced Technology for Ultra Rapid Networks (2000-2003)  
MEDEA T204 Advanced Si Ge BICMOS Technologies for RF applications (2001-2002)  
MEDEA+ T206 CMOS SOI for low Power logic and RF Wireless (2002-2005)