



Thematic Network on Silicon on Insulator Technology, Devices and Circuits.  
[IST-1-506653-CA]

## EUROSIO "Who is Who" Guide

<b>Name of the organisation</b>	
<i>Organization Legal name:</i>	<b>Universitat Rovira i Virgili</b>
<i>Organization short name:</i>	URV
<i>Internet homepage:</i>	<b><a href="http://www.urv.es">http://www.urv.es</a></b>

<b>Contact person</b>			
<i>Name:</i>	<i>Iñiguez</i>	<i>Title:</i>	<i>Prof</i>
<i>First name:</i>	<i>Benjamin</i>	<i>Sex:</i>	<i>Male</i>
<i>Phone:</i>	<i>+34 977 558521</i>	<i>E-mail:</i>	<i><a href="mailto:binyigue@etse.urv.es">binyigue@etse.urv.es</a></i>
<i>Postal Address</i>	<i>Department of Electrical, Electronic and Automatic Engineering, 26 Avinguda dels Països Catalans, Tarragona, 43007, SPAIN</i>		

### **Other Senior Researchers:** (up to 10 names, please include e-mail address)

Prof. Lluís F. Marsal ([lmarsal@etse.urv.es](mailto:lmarsal@etse.urv.es))

Prof. Josep Pallarès ([jpallare@etse.urv.es](mailto:jpallare@etse.urv.es))

Prof. Roger Cabré ([rcabre@etse.urv.es](mailto:rcabre@etse.urv.es))

### **Experience and expertise fields:** (50 words)

Our main research activities are the simulation, characterisation and modelling of advanced semiconductor devices, in particular nano-crystalline Si and organic Thin-Film Transistors and thin-film SOI MOSFETs. The group is currently working in the development of compact models for nanoscale SOI MOS devices, such as Double Gate and Surrounding Gate MOSFETs.

### **Facilities and Equipment:**

#### *Characterisation*

Prober Karl Süss PM5. Impedance Analyzer HP4192A (C-f)

Capacimeter HP4280A (C-V, C-t). Electrometer Keithley 619

Temperature controller (20 °C - 200 °C). Parametric analyzer HP4145B (I-V)

ARC SpectraPro-150

#### *Modelling & Simulation*

Synopsis package. NANOMOS simulator

AIM-Spice source code for device model development

### **Three last international research projects:**

PROJECT: "Characterisation and Modelling of SiC Thin Film Transistors" (ACI2002-34), 2003-2004 (Bilateral Research Action with CINVESTAV, Mexico City)

PROJECT: "Silicon Nanodevices" (SINANO), Network of Excellence, 2004-2006

PROJECT: "EUROSIO", Coordination Action (Thematic Network), 2004-2005